



Sheet 1 of 4

FORM PTO-1449 (REV. 6-89)		U.S. DEPARTMENT OF COMMERCE Patent and Trademark Office		Attorney's Docket No. 21153-05920	Serial No. 10/020,572
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)		Applicant		Sol P. DiJaili	
		Filing Date December 14, 2001		Group Art Unit 3662	

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
9m9	A	6,335,992 B1	1-1-02	Bala, et al.	385	17	2-15-00
	B	6,333,799 B1	12-25-01	Bala, et al.	359	128	1-6-98
	C	6,317,531 B1	11-13-01	Chen, et al.	385	17	
	D	6,128,115	10-3-00	Shiragaki	359	128	
	E	6,115,517	9-5-00	Shiragaki, et al.	385	24	
	F	6,061,156	5-9-00	Takeshita, et al.	359	117	
	G	5,999,293	12-7-99	Manning	359	139	
	H	5,771,320	6-23-98	Stone	385	16	
	I	5,748,653	5-5-98	Parker, et al.	372	8	
	J	5,436,759	7-25-95	DiJaili, et al.	359	333	
	K	5,305,412	4-19-94	Paoli	385	122	
	L	5,299,054	3-29-94	Geiger	359	251	
	M	4,794,346	12-27-88	Miller	330	4.3	
9m9	N	3,828,231	8-6-74	Yamamoto	357	30	
9m9	O	3,467,906	9-16-69	Cornely, et al.	330	4.3	

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
9m9	P	56006492	1-23-81	Japan	H01S	3/18		No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

9m9	Q	Alcatel, "Alcatel Optronics introduces a Gain-Clamped Semiconductor Optical Amplifier," <i>Press Release for Immediate Publication</i> , OFC '98, San Jose (Feb. 1998), 1 unnumbered page.
9m9	R	Diez, S. et al., "Gain-Transparent SOA-Switch for High-Bitrate OTDM Add/Drop Multiplexing," <i>IEEE Photonics Technology Letters</i> , Vol. 11, No. 1 (Jan. 1999), pages 60-62.

EXAMINER Mark Heiner DATE CONSIDERED 09/07/2004

EXAMINER: Initial if references considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



FORM PTO-1449 (REV. 6-89)		U.S. DEPARTMENT OF COMMERCE Patent and Trademark Office		Attorney's Docket No. 21153-05920	Serial No. 10/020,572		
INFORMATION DISCLOSURE CITATION				Applicant Sol P. DiJaili			
(Use several sheets if necessary)				Filing Date December 14, 2001	Group Art Unit 3662		
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes
							No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
9m90	S	Diez, S. et al., "Novel Gain-Transparent SOA-Switch for High Bitrate OTDM Add/Drop Multiplexing," ECOC '98, Madrid, Spain (Sept. 1998), pages 461-462.					
	T	Diez, S. et al., "All-Optical Switch for TDM and WDM/TDM Systems Demonstrated in a 640Gbit/s Demultiplexing Experiment," <i>Electronics Letters</i> , Vol. 34, No. 8 (April 16, 1998), pages 803-805.					
	U	Dorgeuille, F. et al., "1.28 Tbit/s Throughout 8/Spl Times/8 Optical Switch Based on Arrays of Gain-Clamped Semiconductor Optical Amplifier Gates," OFCC 2000, Baltimore, MD, March 2000, Vol. 4, pages 221-223.					
	V	Dorgeuille, F. et al., "Fast Optical Amplifier Gate Array for WDM Routing and Switching Applications," OFC'98 Technical Digest, pages 42-44.					
	W	Doussiere, P. et al., "Clamped Gain Travelling Wave Semiconductor Optical Amplifier for Wavelength Division Multiplexing Applications," Maui, Hawaii, Sept. 19-23, 1994, New York, IEEE, US, Vol. Conf. 14 (9/14/94), pages 185-186.					
	X	Evankow, JosephD. et al., "Photonic Switching Modules Designed With Laser Diode Amplifiers," <i>IEEE Journal on Selected Areas in Communications</i> , Vol. 6, No. 7 (Aug. 1988), pages 1087-1095.					
	Y	Fernier, B. et al., "Fast (300 ps)-Polarization-Insensitive Semiconductor Optical Amplifier Switch With Low Driving Current (70 mA)," Semiconductor Laser Conference, September 1992, pages 130-131.					
	Z	Fjelde, T. et al., "Influence of RZ and NRZ Signal Format on the High-Speed Performance of Gain-Clamped Semiconductor Optical Amplifiers," <i>Research Center COM</i> , Lyngby, Denmark, pages 87-89.					
	AA	Fouquet, J.E. et al., "Compact, Scalable Fiber Optic Cross-Connect Switches," <i>Digest of the LEOS Summer Topical Meetings</i> , San Diego, CA, July 1999, pages 59-60.					
	BB	Ibrahim, Magdy M., "Photonic Switch Using Surface-Emitting Laser Diode and APD," NRSC '99, Cairo, Egypt, Feb. 1999, pages D7 1-D7 8.					
	CC	Jeong, Gibong et al., "Gain Optimization in Switches Based on Semiconductor Optical Amplifiers," <i>Journal of Lightwave Technology</i> , Vol. 13, No. 4 (April 1995), pages 598-605.					
9m90	DD	Kitamura, Shotaro, et al., "Spot-Size Converter Integrated Semiconductor Optical Amplifiers for Optical Gate Applications," <i>IEEE Journal of Quantum Electronics</i> , Vol. 35, No. 7 (July 1999), pages 1067-1074.					
EXAMINER	Mark Hettner			DATE CONSIDERED	09/07/04		
EXAMINER: Initial if references considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered.							
Include copy of this form with next communication to applicant.							



FORM PTO-1449 (REV. 6-89)		U.S. DEPARTMENT OF COMMERCE Patent and Trademark Office		Attorney's Docket No. 21153-05920	Serial No. 10/020,572
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)		Applicant		Sol P. DiJaili	
		Filing Date December 14, 2001		Group Art Unit 3662	

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS

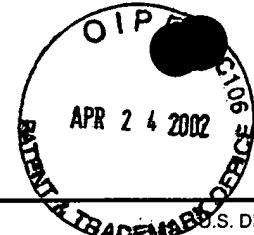
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

9/20	EE	Koyama, F. et al., "Multiple-Quantum-Well GaInAs/GaInAsP Tapered Broad-Area Amplifiers with Monolithically Integrated Waveguide Lens for High-Power Applications," <i>IEEE Photonics Technology Letters</i> , Vol. 5, No. 8 (August 1993), pages 916-919.
	FF	Leuthold, Juerg et al., "All-Optical Space Switches with Gain and Principally Ideal Extinction Ratios," <i>IEEE Journal of Quantum Electronics</i> , Vol. 34, No. 4 (April 1998), pages 622-633.
	GG	Luo, G. et al., "Experimental and Theoretical Analysis of Relaxation-Oscillations and Spectral Hole Burning Effects in All-Optical Gain-Clamped EDFA's for WDM Networks," <i>Journal of Lightwave Technology</i> , Vol. 16, No. 4 (April 1998), pages 527-533.
	HH	McAdams, Larry R. et al., "Linearizing High Performance Semiconductor Optical Amplifiers: Techniques and Performance," LEOS Presentation (1996), Thursday 11:00 AM, pages 363-364.
	II	Mork, J., et al., "Semiconductor Devices for All-Optical Signal Processing: Just How Fast Can They Go?," LEOS '99, San Francisco, CA, November 1999, Vol. 2, pages 900-901.
	JJ	Mutalik, Venkatesh G. et al., "Analog Performance of 1310-nm Gain-Clamped Semiconductor Optical Amplifiers," <i>OFC '97 Technical Digest</i> , Thursday 11:15 AM, pages 266-267.
	KK	Panajotov, K. et al., "Polarisation Switching In Proton-Implanted VCSELs," <i>Digest of the LEOS Summer Topical Meetings</i> , San Diego, CA (July 1999), Thursday 2:45 PM, pages III55-III56.
	LL	Qiu, B.C. et al., "Monolithically Integrated Fabrication of 2 x 2 and 4 x 4 Crosspoint Switches Using Quantum Well Intermixing," Indium Phosphide and Related Materials, Conference Proceedings, Williamsburg, VA (May 2000), pages 415-418.
	MM	Scheuer, J. et al., "Nonlinear On-Switching of High Spatial Frequency Patterns in Ring Vertical Cavity Surface Emitting Lasers," LEOS '99, San Francisco, CA (Nov. 1999), Vol. 1, pages 123-124.
9/20	NN	Soto, H. et al., "All-Optical Switch Demonstration Using a Birefringence Effect In A Semiconductor Optical Amplifier," <i>CLEO Pacific Rim '99</i> , pages 888-889.

EXAMINER	Mark Hettner	DATE CONSIDERED	09/07/04
----------	--------------	-----------------	----------

EXAMINER: Initial if references considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Sheet 4 of 4

FORM PTO-1449 (REV. 6-89)		U.S. DEPARTMENT OF COMMERCE Patent and Trademark Office		Attorney's Docket No. 21153-05920	Serial No. 10/020,572		
INFORMATION DISCLOSURE CITATION <small>(Use several sheets if necessary)</small>		Applicant		Sol P. DiJaili			
		Filing Date December 24, 2001		Group Art Unit 3662			
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes
							No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
QWAD	OO	Soulage, G. et al., "Clamped Gain Travelling Wave Semiconductor Optical Amplifier as a Large Dynamic Range Optical Gate," Alcatel Alsthom Recherche, route de Nozay, 91460 Marcoussis, France, undated, 4 unnumbered pages.					
	PP	Tai, Chien et al., "Dynamic Range and Switching Speed Limitations of an N x N Optical Packet Switch Based on Low-Gain Semiconductor Optical Amplifiers," <i>Journal of Lightwave Technology</i> , Vol. 14, No. 4 (April 1996), pages 525-533.					
	QQ	Tiemeijer, L.F. et al., "High-Gain 1310 nm Semiconductor Optical Amplifier Modules with a Built-in Amplified Signal Monitor for Optical Gain Control," <i>IEEE Photonics Technology Letters</i> , Vol. 9, No. 3 (March 1997), pages 309-311.					
	RR	Tiemeijer, L.F. et al., "Reduced Intermodulation Distortion in 1300 nm Gain-Clamped MQW Laser Amplifiers," <i>IEE Photonics Technology Letters</i> , Vol. 7, No. 3 (March 1995), pages 284-286.					
	SS	Toptchiyski, Gueorgui et al., "Time-Domain Modeling of Semiconductor Optical Amplifiers for OTDM Applications," <i>Journal of Lightwave Technology</i> , Vol. 17, No. 12 (Dec. 1999), pages 2577-2583					
	TT	van Roijen, R. et al., "Over 15 dB Gain From A Monolithically Integrated Optical Switch With An Amplifier," <i>IEEE Photonics Technology Letters</i> , Vol. 5, No. 5 (May 1993), pages 529-531.					
QWAD	UU	Yoshimoto, N. et al., "Spot-Size Converted Polarization-Insensitive SOA Gate With A Vertical Tapered Submicrometer Strip Structure," <i>IEEE Photonics Technology Letters</i> , Vol. 10, No. 4 (April 1998), pages 510-512.					
EXAMINER <i>Mark H. Ellner</i>			DATE CONSIDERED <i>09/07/04</i>				
EXAMINER: Initial if references considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							